Tool Make & Model

ADVANTEST: T5371

Tool Configuration / Specifications

Target Devices	DRAMs, SDRAMs, CDRAMs, SRAMs, flash memories, Direct RDRAMs, EPROMs, Masked ROMs, etc
Test Speed	70 MHz
Overall Timing Accuracy	+/-800 ps
Simultaneous Testing	Up to 128 devices (x8 or x9 bits)
Test Heads	Up to two stations HV driver comes standard
Timing Generation	27 timing edges/pin (and/or) 16 timing sets/pin
DC Test Units	Up to 32 units/station
Programmable Power Supply	Up to 128 units/station
Options	MRA4 (Memory Repair Analyzer) or AFM

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