System Type: MagnumV_EV_System

Config.ini TIU Type: Mag5_DSA_128

Flow: Diagnostics

Using 128 Pin Assignments

Loading Pe VCAL Data Calibrated on Mon Mar 10 18:30:06 2025 with release f7.29.4.1

Loading Dp VCAL Data Calibrated on Mon Mar 10 18:29:41 2025 with release f7.29.4.1

Magnum Version: MagnumV_EV_System

HostName: fmnsgmag5011

UserName: Administrator

Nextest Release: C:\nextest\f7.29.4.1

Test Program: Y:\nextest\f7.29.4.1\Bin\TimingCalDx_MagV.exe

System Site#: 1, Chassis#: 1, Slot#: 1 Board Site#: 1

HSB4DFE , PWB: 521403, Rev: 1, PWA: 517352, Rev: 14

PE256D_1 , PWB: 518540, Rev: 4, PWA: 519703, Rev: 8

MagnumV section DSA ASSY: , Rev: -1, PWA: , Rev: -1,

PWB: , Rev: -1

System Site#: 1, Chassis#: 1, Slot#: 1 Board Site#: 1

Select START when ready

The test program is loaded [C:\nextest\f7.29.4.1|a130e37|using map

type NC].

TestStarted(1)...

Started: Mon Mar 10 18:53:31 2025

Initializing

Checking PE Path Continuity [continuity_tb]

Test Summary:

Pass Site 1: Chassis 1 - Slot 1

Done: Mon Mar 10 18:53:31 2025

TestDone...bin = builtin_Pass,builtin_Pass

The test program is unloaded