Initiating Unison diag sequence

Reading Unison inventory files ...
Verifying that configured extm boards are installed
...All configured extm boards were found:
Reading Unison configuration files ...
---------- Unison run-time Executive program ----------
U5.2.2.1: Library revision: 600.0
HW License: HBird 2 Channel Option, HBird 250MHz Option, Certificate 8754

Board Licenses: M32M - 16 board/s - 32M Vector Memory
Program Loaded = (Null) SystemDiagnostics

########## Build : U5.2.2.1 ##########
System Meter : slot 208 LTXC_HP34401A RACK RS232

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*** Diagnostic (DPIN96 Checker) Run Begins At ***
*** Wed Mar 01 11:19:59 2023 ***
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****
Test: DMM Checker Wed Mar 1 11:19:59 2023
Test: DPIN96 Clean Up Test Wed Mar 1 11:20:08 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPIN96 Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 11:20:08 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 8 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32

```
|---------------------------
| Slot | Test Title
|------|
| 8    |
|---------------------------
| .    | DMM Checker
| .    | DPIN96 Clean Up Test
|---------------------------
"." = Pass   "F" = Fail   "u" = Untested   "A" = Aborted   " " = Not Applicable

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********** Result Display Format Rev 01 ***************************
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Software Revision   U5.2.2.1
Summary             PASS

Host Name           FTDX005
Run Completed       Wed Mar 01 11:20:08 2023

Support Boards      None

-----------------------------------------------------------------
| Slot |  Status  |    P/N       | VRev  |    S/N     | Type
-----------------------------------------------------------------
|   8  |   PASS   | 982-7419     | 00.PF |            | PHX_DD1096_32
-----------------------------------------------------------------

|---------------------------
| Slot | Test Title
|------|
| 8    |
|---------------------------
| .    | DMM Checker
| .    | DPIN96 Clean Up Test
|---------------------------
"." = Pass   "F" = Fail   "u" = Untested   "A" = Aborted   " " = Not Applicable

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################################  SMC+ summary  ###################################

Loops : 1

Version:    U5.2.2.1
Tester:     FTDX005
Created:    2023-03-01 11:20:08
```

```
-------------------------------------------------------------------------------------------------------------------------
| Session tasks              | Instruments | Loadboards | Total | Aborted | Failed | Last Run           | Duration (sec.) |
|----------------------------+-------------+------------+-------+---------+--------+--------------------+-----------------|
| All tasks                  |             |            |     2 |       0 |      0 | 2023-03-01 11:20:08 |          09.070 |
|    DPIN96 Checker          | DD1096_32   | Removed    |     2 |       0 |      0 | 2023-03-01 11:20:08 |          09.070 |
|       DPIN96 Basic Checker | DD1096_32   | Removed    |     1 |       0 |      0 | 2023-03-01 11:20:08 |          09.063 |
|          DMM Chk           | DD1096_32   | Removed    |     1 |       0 |      0 | 2023-03-01 11:20:08 |          09.063 |
|       Cleanup              | DD1096_32   | Removed    |     1 |       0 |      0 | 2023-03-01 11:20:08 |          00.007 |
-------------------------------------------------------------------------------------------------------------------------

#########################################################################################


########## Build : U5.2.2.1 ##########
System Meter : slot 208  LTXC_HP34401A  RACK RS232


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***                                                 ***
***                                                 ***
***                                                 ***
***          Diagnostic (THCTL Checker) Run Begins At          ***
***               Wed Mar 01 11:20:36 2023                ***
***                                                 ***
***                                                 ***
***                                                 ***
*************************************************************************************
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*************************************************************************************
Test: Board ID                              Wed Mar  1 11:20:36 2023

IMA0_Slot30 Slot[30]:LTXC_PHX_THCTL, PN# 979-1297, SN#     , VRev 10.MD

Test: CPI Data Sequencer Memory Test              Wed Mar  1 11:20:36 2023
Test: Register Test                         Wed Mar  1 11:20:37 2023
Test: Temperature Sensor Test                     Wed Mar  1 11:20:37 2023
Test: Fan Speed Test                        Wed Mar  1 11:20:38 2023
Test: Event Bus Test                        Wed Mar  1 11:20:38 2023
Test: Cal Bus Relay Test                      Wed Mar  1 11:20:39 2023
Test: TMU Mux Test                          Wed Mar  1 11:20:41 2023
Test: DC Force Voltage Chk                      Wed Mar  1 11:20:41 2023
Test: DC Force Current Chk                    Wed Mar  1 11:20:49 2023
Test: DC Measure Voltage Chk                    Wed Mar  1 11:21:16 2023
Test: System EEPROM Validation                      Wed Mar  1 11:21:17 2023
Test: THCTL Clean Up Test                     Wed Mar  1 11:21:17 2023


******************** Result Display Format Rev 01 ****************************
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```

Software Revision U5.2.2.1
Summary THCTL Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 11:21:17 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
30	PASS	979-1297	10.MD		PHX_THCTL

Slot	Test Title
3	
0	
.	Board ID
.	CPI Data Sequencer Memory Test
.	Register Test
.	Temperature Sensor Test
.	Fan Speed Test
.	Event Bus Test
.	Cal Bus Relay Test
.	TMU Mux Test
.	DC Force Voltage Chk
.	DC Force Current Chk
.	DC Measure Voltage Chk
.	System EEPROM Validation
.	THCTL Clean Up Test

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (THCTL Calibration) Run Begins At ***
*** Wed Mar 01 11:21:17 2023 ***
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Test: DMM Rear Terminals Cal Wed Mar 1 11:21:17 2023
Test: DC Force Voltage Cal Wed Mar 1 11:21:19 2023
Test: DC Force Current Cal Wed Mar 1 11:21:21 2023
Test: DC Measure Voltage Cal Wed Mar 1 11:21:34 2023
Test: THCTL Clean Up Test Wed Mar 1 11:21:35 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary THCTL Calibration PASS

Host Name FTDX005
Run Completed Wed Mar 01 11:21:35 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
30	PASS	979-1297	10.MD		PHX_THCTL

Slot	Test Title
3	
0	
.	DMM Rear Terminals Cal
.	DC Force Voltage Cal
.	DC Force Current Cal
.	DC Measure Voltage Cal
.	THCTL Clean Up Test

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (THCTL Verify) Run Begins At ***
*** Wed Mar 01 11:21:35 2023 ***
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Test: DMM Rear Terminals Vrf                Wed Mar  1 11:21:35 2023
Test: DC Force Voltage Vrf                  Wed Mar  1 11:21:35 2023
Test: DC Force Current Vrf                  Wed Mar  1 11:21:43 2023
Test: DC Measure Voltage Vrf                Wed Mar  1 11:22:11 2023
Test: THCTL Clean Up Test                   Wed Mar  1 11:22:12 2023


******************** Result Display Format Rev 01 ****************************
********************************************************************************

Software Revision   U5.2.2.1
Summary             THCTL Verify PASS

Host Name           FTDX005
Run Completed       Wed Mar 01 11:22:13 2023

Support Boards      None

-----------------------------------------------------------------
| Slot |  Status  |    P/N      | VRev |    S/N     | Type
-----------------------------------------------------------------
|  30  |   PASS   | 979-1297    | 10.MD |            | PHX_THCTL
-----------------------------------------------------------------

|---------------------------
| Slot | Test Title
|------|
| 3    |
| 0    |
|---------------------------
| .    | DMM Rear Terminals Vrf
| .    | DC Force Voltage Vrf
| .    | DC Force Current Vrf
| .    | DC Measure Voltage Vrf
| .    | THCTL Clean Up Test
|---------------------------
"." = Pass   "F" = Fail   "u" = Untested   "A" = Aborted   " " = Not Applicable

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***                                                  ***
***                                                  ***
***                                                  ***
***              Diagnostic (SDU Checker) Run Begins At              ***
***                   Wed Mar 01 11:22:13 2023                   ***
***                                                  ***
***                                                  ***
```

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***                                                                          ***
********************************************************************************
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********************************************************************************
Test: Board ID                                Wed Mar  1 11:22:13 2023

IMA0_Slot0 Slot[0]:LTXC_PHX_SDU, PN# 979-1300, SN# , VRev 10.AF

Test: CPI Load Port Test                      Wed Mar  1 11:22:13 2023
Test: CPI Temperature Sensor Test             Wed Mar  1 11:22:13 2023
Test: CPI Data Sequencer Memory Test          Wed Mar  1 11:22:13 2023
Test: CPI Internal Event Bus Test             Wed Mar  1 11:22:15 2023
Test: DMA Burst Test                          Wed Mar  1 11:22:15 2023
Subtest: Hydrant 1: Name=CPI_LFSR, Size=0xffff, addrSeed=0xb276c9c2
Subtest: Hydrant 2: Name=CPI_LFSR, Size=0xffff, addrSeed=0xb2e046ae
Subtest: Hydrant 3: Name=SBUS_LFSR, Size=0xffff, addrSeed=0xb348a00a
Test: SDU Register Test                       Wed Mar  1 11:22:15 2023
Test: SDU Reference Clock Status              Wed Mar  1 11:22:15 2023
Test: SDU CBITs Test                          Wed Mar  1 11:22:15 2023
Test: SDU DUT Board Utility Power Test        Wed Mar  1 11:22:15 2023
Test: SDU DC Bus Test                         Wed Mar  1 11:22:17 2023
Test: SDU DUT Board Event Bus Test            Wed Mar  1 11:22:18 2023
Test: SDU DUT Board Register Bus Test         Wed Mar  1 11:22:18 2023
Test: SDU DUT Board Status Signal Test        Wed Mar  1 11:22:18 2023
Test: SDU DUT Board I2C Bus Test              Wed Mar  1 11:22:18 2023
Test: SDU DUT Board I2C EEPROM Bus Test       Wed Mar  1 11:22:18 2023
Test: SDU Scope Sync Test                     Wed Mar  1 11:22:18 2023
Test: SDU DUT Board SPI Bus Test              Wed Mar  1 11:22:18 2023


********** Result Display Format Rev 01 **********
********************************************************************************

Software Revision   U5.2.2.1
Summary             SDU Checker PASS

Host Name           FTDX005
Run Completed       Wed Mar 01 11:22:19 2023

Support Boards      None

-----------------------------------------------------------------
| Slot |  Status  |    P/N      | VRev |    S/N     | Type
-----------------------------------------------------------------
|   0  |   PASS   | 979-1300    | 10.AF |          | PHX_SDU
-----------------------------------------------------------------

|-----------------------------
| Slot | Test Title
|------|
| 0    |
|-----------------------------
| .    | Board ID
| .    | CPI Load Port Test
```

| . | CPI Temperature Sensor Test
| . | CPI Data Sequencer Memory Test
| . | CPI Internal Event Bus Test
| . | DMA Burst Test
| . | SDU Register Test
| . | SDU Reference Clock Status
| . | SDU CBITs Test
| . | SDU DUT Board Utility Power Test
| . | SDU DC Bus Test
| . | SDU DUT Board Event Bus Test
| . | SDU DUT Board Register Bus Test
| . | SDU DUT Board Status Signal Test
| . | SDU DUT Board I2C Bus Test
| . | SDU DUT Board I2C EEPROM Bus Test
| . | SDU Scope Sync Test
| . | SDU DUT Board SPI Bus Test
|---------------------------
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (DPIN96 Checker) Run Begins At ***
*** Wed Mar 01 11:22:19 2023 ***
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****
Test: CPI Load Port Test Wed Mar 1 11:22:19 2023
Test: CPI Data Sequencer Memory Test Wed Mar 1 11:22:19 2023
Test: DMM Rear Terminals Chk Wed Mar 1 11:22:38 2023
Test: Caldiag Init Wed Mar 1 11:22:40 2023
Test: BI Health Check Wed Mar 1 11:22:43 2023
Test: DMM Checker Wed Mar 1 11:22:43 2023
Test: Trigger Reflection Test Wed Mar 1 11:24:40 2023
Test: Base Register Test Wed Mar 1 11:24:41 2023
Test: PATE Register Test Wed Mar 1 11:24:57 2023
Test: Memory Controller Register-Memory Test Wed Mar 1 11:25:22 2023
Test: IC VPRAM exhaustive memory test Wed Mar 1 11:25:22 2023
Test: ADC Test Wed Mar 1 11:25:29 2023
Test: Driver Reference Test Wed Mar 1 11:25:29 2023
Test: Driver Disable Test Wed Mar 1 11:25:54 2023
Test: PMU FV-MV Test Wed Mar 1 11:26:23 2023
Test: PMU FI-MI Test Wed Mar 1 11:26:46 2023
Test: PMU Clamp Test Wed Mar 1 11:27:10 2023

Test: MC DRAM R-W Test Wed Mar 1 11:27:30 2023
Test: MC DRAM Walk-1 Addr Test Wed Mar 1 11:29:33 2023
Test: MC Capture Mem Stress Test Wed Mar 1 11:29:34 2023
Test: MC Capture RAM Walk-1 Addr Test Wed Mar 1 11:31:17 2023
Test: MC Basic Pattern Burst Wed Mar 1 11:31:17 2023
Test: MC MNO bit walk test Wed Mar 1 11:32:46 2023
Test: MC Repeat Test Wed Mar 1 11:33:03 2023
Test: MC Progressive Repeat Test Wed Mar 1 11:34:07 2023
Test: MC Loop Test Wed Mar 1 11:34:26 2023
Test: MC Loop Boundary Test Wed Mar 1 11:35:30 2023
Test: MC Fail on Last Vector Test Wed Mar 1 11:36:34 2023
Test: MC Fail per Channel Diag Wed Mar 1 11:37:38 2023
Test: MC Vector DRAM Integrity Test Wed Mar 1 11:37:55 2023
Test: MC Capture RAM Integrity Test Wed Mar 1 11:39:38 2023
Test: MC - Omni Multi-Period Test Wed Mar 1 11:42:53 2023
Test: Basic Scan Test Wed Mar 1 11:57:04 2023
Test: AC Basic Test Wed Mar 1 11:57:39 2023
Test: DC Comparator Pre-test Wed Mar 1 11:58:04 2023
Test: DC Driver Pre-test Wed Mar 1 11:58:46 2023
Test: DC FV16 Pre-test Wed Mar 1 11:59:02 2023
Test: DC Clamp Pre-test Wed Mar 1 11:59:17 2023
Test: DC Measure Voltage Pre-test Wed Mar 1 11:59:59 2023
Test: DC Force Current Pre-test Wed Mar 1 12:00:14 2023
Test: DC Measure Current Pre-test Wed Mar 1 12:00:31 2023
Test: DC Comparator Measure Current Pre-test Wed Mar 1 12:00:46 2023
Test: DPIN96 Clean Up Test Wed Mar 1 12:01:02 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPIN96 Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 12:01:02 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
2	PASS	982-7419	00.PF		PHX_DD1096_32
3	PASS	982-7419	00.PF		PHX_DD1096_32
4	PASS	982-7419	00.PF		PHX_DD1096_32
5	PASS	982-7419	00.PF		PHX_DD1096_32
6	PASS	982-7419	00.PF		PHX_DD1096_32
7	PASS	982-7419	00.PF		PHX_DD1096_32
8	PASS	982-7419	00.PF		PHX_DD1096_32
9	PASS	982-7419	00.OA		PHX_DD1096_32
12	PASS	982-7419	00.PF		PHX_DD1096_32
13	PASS	982-7419	00.PF		PHX_DD1096_32
14	PASS	982-7419	00.PF		PHX_DD1096_32
15	PASS	982-7419	00.OA		PHX_DD1096_32

| 16 | PASS | 982-7419 | 00.PF | 1113101 | PHX_DD1096_32

|---
| Slot | Test Title
|---|
| 11111 |
| 2345678923456 |
|---
| ..... | CPI Load Port Test
| ..... | CPI Data Sequencer Memory Test
| ..... | DMM Rear Terminals Chk
| ..... | Caldiag Init
| ..... | BI Health Check
| ..... | DMM Checker
| ..... | Trigger Reflection Test
| ..... | Base Register Test
| ..... | PATE Register Test
| ..... | Memory Controller Register-Memory Test
| ..... | IC VPRAM exhaustive memory test
| ..... | ADC Test
| ..... | Driver Reference Test
| ..... | Driver Disable Test
| ..... | PMU FV-MV Test
| ..... | PMU FI-MI Test
| ..... | PMU Clamp Test
| ..... | MC DRAM R-W Test
| ..... | MC DRAM Walk-1 Addr Test
| ..... | MC Capture Mem Stress Test
| ..... | MC Capture RAM Walk-1 Addr Test
| ..... | MC Basic Pattern Burst
| ..... | MC MNO bit walk test
| ..... | MC Repeat Test
| ..... | MC Progressive Repeat Test
| ..... | MC Loop Test
| ..... | MC Loop Boundary Test
| ..... | MC Fail on Last Vector Test
| ..... | MC Fail per Channel Diag
| ..... | MC Vector DRAM Integrity Test
| ..... | MC Capture RAM Integrity Test
| ..... | MC - Omni Multi-Period Test
| ..... | Basic Scan Test
| ..... | AC Basic Test
| ..... | DC Comparator Pre-test
| ..... | DC Driver Pre-test
| ..... | DC FV16 Pre-test
| ..... | DC Clamp Pre-test
| ..... | DC Measure Voltage Pre-test
| ..... | DC Force Current Pre-test
| ..... | DC Measure Current Pre-test
| ..... | DC Comparator Measure Current Pre-test
| ..... | DPIN96 Clean Up Test
|---

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (DPIN96 Calibration) Run Begins At ***
*** Wed Mar 01 12:01:02 2023 ***
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Test: Init Cal Files Wed Mar 1 12:01:02 2023
Test: DMM Rear Terminals Cal Wed Mar 1 12:01:02 2023
Test: External DC Cal Wed Mar 1 12:01:03 2023
Test: DC Gnd Level Cal Wed Mar 1 12:17:12 2023
Test: DC Comparator Cal Wed Mar 1 12:17:28 2023
Test: DC Driver Cal Wed Mar 1 12:22:56 2023
Test: DC FV16 Cal Wed Mar 1 12:26:27 2023
Test: DC Clamp Cal Wed Mar 1 12:29:16 2023
Test: DC Measure Voltage Cal Wed Mar 1 12:31:58 2023
Test: DC Force Current Cal Wed Mar 1 12:34:06 2023
Test: DC Measure Current Cal Wed Mar 1 12:41:34 2023
Test: DC Comparator Measure Current Cal Wed Mar 1 13:10:36 2023
Test: AC Temperature Compensation Wed Mar 1 13:24:20 2023
Test: DPIN96 Clean Up Test Wed Mar 1 13:26:58 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPIN96 Calibration PASS

Host Name FTDX005
Run Completed Wed Mar 01 13:26:58 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
2	PASS	982-7419	00.PF		PHX_DD1096_32
3	PASS	982-7419	00.PF		PHX_DD1096_32
4	PASS	982-7419	00.PF		PHX_DD1096_32
5	PASS	982-7419	00.PF		PHX_DD1096_32
6	PASS	982-7419	00.PF		PHX_DD1096_32

```
|  7 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
|  8 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
|  9 |  PASS  | 982-7419   | 00.OA |            | PHX_DD1096_32
| 12 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
| 13 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
| 14 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
| 15 |  PASS  | 982-7419   | 00.OA |            | PHX_DD1096_32
| 16 |  PASS  | 982-7419   | 00.PF |            | PHX_DD1096_32
-----------------------------------------------------------------

|-----------------------------------
| Slot         | Test Title
|--------------|
|       11111 |
| 2345678923456 |
|-----------------------------------
| ..... | Init Cal Files
| ..... | DMM Rear Terminals Cal
| ..... | External DC Cal
| ..... | DC Gnd Level Cal
| ..... | DC Comparator Cal
| ..... | DC Driver Cal
| ..... | DC FV16 Cal
| ..... | DC Clamp Cal
| ..... | DC Measure Voltage Cal
| ..... | DC Force Current Cal
| ..... | DC Measure Current Cal
| ..... | DC Comparator Measure Current Cal
| ..... | AC Temperature Compensation
| ..... | DPIN96 Clean Up Test
|-----------------------------------
"." = Pass   "F" = Fail   "u" = Untested   "A" = Aborted   " " = Not Applicable

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***                                                  ***
***                                                  ***
***                                                  ***
***            Diagnostic (DPIN96 Verify) Run Begins At            ***
***                 Wed Mar 01 13:26:58 2023                 ***
***                                                  ***
***                                                  ***
***                                                  ***
********************************************************************************
********************************************************************************
********************************************************************************
Test: DMM Rear Terminals Vrf                  Wed Mar  1 13:26:58 2023
Test: External DC Verify                     Wed Mar  1 13:26:59 2023
Test: DC Comparator Verify                    Wed Mar  1 13:31:41 2023
```

Test: DC Driver Verify Wed Mar 1 13:34:25 2023
Test: DC FV16 Verify Wed Mar 1 13:35:22 2023
Test: DC Clamp Verify Wed Mar 1 13:35:52 2023
Test: DC Measure Voltage Verify Wed Mar 1 13:37:50 2023
Test: DC Force Current Verify Wed Mar 1 13:38:08 2023
Test: DC Measure Current Verify Wed Mar 1 13:42:17 2023
Test: DC Comparator Measure Current Verify Wed Mar 1 14:00:19 2023
Test: DPIN96 Clean Up Test Wed Mar 1 14:12:23 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPIN96 Verify PASS

Host Name FTDX005
Run Completed Wed Mar 01 14:12:23 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
2	PASS	982-7419	00.PF		PHX_DD1096_32
3	PASS	982-7419	00.PF		PHX_DD1096_32
4	PASS	982-7419	00.PF		PHX_DD1096_32
5	PASS	982-7419	00.PF		PHX_DD1096_32
6	PASS	982-7419	00.PF		PHX_DD1096_32
7	PASS	982-7419	00.PF		PHX_DD1096_32
8	PASS	982-7419	00.PF		PHX_DD1096_32
9	PASS	982-7419	00.OA		PHX_DD1096_32
12	PASS	982-7419	00.PF		PHX_DD1096_32
13	PASS	982-7419	00.PF		PHX_DD1096_32
14	PASS	982-7419	00.PF		PHX_DD1096_32
15	PASS	982-7419	00.OA		PHX_DD1096_32
16	PASS	982-7419	00.PF		PHX_DD1096_32

Slot	Test Title
11111	
2345678923456	
.....	DMM Rear Terminals Vrf
.....	External DC Verify
.....	DC Comparator Verify
.....	DC Driver Verify
.....	DC FV16 Verify
.....	DC Clamp Verify
.....	DC Measure Voltage Verify
.....	DC Force Current Verify
.....	DC Measure Current Verify

| ..... | DC Comparator Measure Current Verify
| ..... | DPIN96 Clean Up Test
|----------
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** ***
*** Diagnostic (DPS16 Checker) Run Begins At ***
*** Wed Mar 01 14:12:23 2023 ***
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Test: CPI Load Port Test Wed Mar 1 14:12:23 2023
Test: CPI Data Sequencer Memory Test Wed Mar 1 14:12:23 2023
Test: Read FPGA Version Wed Mar 1 14:12:25 2023
Slot 1
U82 FGPA FIRMWARE 1
U112 FGPA FIRMWARE 61700

Test: Check Dps16 Memory Wed Mar 1 14:12:25 2023
Test: Dps16 Clean Up Test Wed Mar 1 14:12:25 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPS16 Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 14:12:26 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 1 | PASS | 982-6136 | 00.NA | | PHX_DPS16

|----------

| Slot | Test Title
|------|
| 1 |
|---
| . | CPI Load Port Test
| . | CPI Data Sequencer Memory Test
| . | Read FPGA Version
| . | Check Dps16 Memory
| . | Dps16 Clean Up Test
|---
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** ***
*** Diagnostic (DPS16 Calibration) Run Begins At ***
*** Wed Mar 01 14:12:26 2023 ***
*** ***
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****
Test: DMM Rear Terminals Cal Wed Mar 1 14:12:26 2023
Test: Measure Dps16 Ref Voltages and Resistors Wed Mar 1 14:12:28 2023

Saving the Measured Referance Voltages and Resistors for Slot 1

Test: Calibrate Dps16 ADC Wed Mar 1 14:12:49 2023
Test: Calibrate Dps16 Force Voltage Wed Mar 1 14:12:49 2023
Test: Calibrate Dps16 Current Measure Wed Mar 1 14:12:52 2023
Test: Calibrate Dps16 Current Limit Wed Mar 1 14:13:16 2023
Test: Dps16 Clean Up Test Wed Mar 1 14:13:41 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPS16 Calibration PASS

Host Name FTDX005
Run Completed Wed Mar 01 14:13:41 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
1	PASS	982-6136	00.NA		PHX_DPS16

Slot	Test Title
1	
.	DMM Rear Terminals Cal
.	Measure Dps16 Ref Voltages and Resistors
.	Calibrate Dps16 ADC
.	Calibrate Dps16 Force Voltage
.	Calibrate Dps16 Current Measure
.	Calibrate Dps16 Current Limit
.	Dps16 Clean Up Test

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (DPS16 Verify) Run Begins At ***
*** Wed Mar 01 14:13:41 2023 ***
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Test: Verify the Dps16 ADC Wed Mar 1 14:13:41 2023
Test: Verify Dps16 Force Voltage Wed Mar 1 14:13:42 2023
Test: Verify Dps16 Measure Current Wed Mar 1 14:13:44 2023
Test: Verify Dps16 Current Limit Wed Mar 1 14:15:39 2023
Test: Verify Dps16 4A Gang Mode Wed Mar 1 14:18:32 2023
Test: Verify Dps16 8A Gang Mode Wed Mar 1 14:19:03 2023
Test: Verify Dps16 16A Gang Mode Wed Mar 1 14:19:19 2023
Test: Check Dps16 site control Wed Mar 1 14:19:27 2023
Test: Dps16 Clean Up Test Wed Mar 1 14:19:29 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary DPS16 Verify PASS

Host Name FTDX005
Run Completed Wed Mar 01 14:19:29 2023

Support Boards None

```
-------------------------------------------------------------
| Slot |  Status  |    P/N      | VRev  |    S/N     | Type
-------------------------------------------------------------
|   1  |   PASS   | 982-6136    | 00.NA |            | PHX_DPS16
-------------------------------------------------------------
```

```
|--------------------------
| Slot | Test Title
|------|
| 1    |
|--------------------------
| .    | Verify the Dps16 ADC
| .    | Verify Dps16 Force Voltage
| .    | Verify Dps16 Measure Current
| .    | Verify Dps16 Current Limit
| .    | Verify Dps16 4A Gang Mode
| .    | Verify Dps16 8A Gang Mode
| .    | Verify Dps16 16A Gang Mode
| .    | Check Dps16 site control
| .    | Dps16 Clean Up Test
|--------------------------
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"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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***                                          ***
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***                                          ***
***          Diagnostic (HDVI Checker) Run Begins At          ***
***               Wed Mar 01 14:19:29 2023               ***
***                                          ***
***                                          ***
***                                          ***
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Test: CPI Load Port Test Wed Mar 1 14:19:29 2023
Test: CPI Data Sequencer Memory Test Wed Mar 1 14:19:29 2023

Starting the test not parallel!!!

Test: Check local bus Wed Mar 1 14:19:37 2023

Test: Check local bus Wed Mar 1 14:19:39 2023

Starting the test not parallel!!!

Test: Check high speed bus Wed Mar 1 14:19:41 2023

Test: Check high speed bus Wed Mar 1 14:19:43 2023

Starting the test not parallel!!!

Test: Check VIS communication Wed Mar 1 14:19:46 2023

Test: Check VIS communication Wed Mar 1 14:20:12 2023

Starting the test not parallel!!!

Test: Check PAS communication Wed Mar 1 14:20:38 2023

Test: Check PAS communication Wed Mar 1 14:20:40 2023

Starting the test not parallel!!!

Test: Check SRAM Wed Mar 1 14:20:42 2023
Checking memory bank 0...
Checking memory bank 1...
Checking memory bank 2...
Checking memory bank 3...
Checking memory bank 4...
Checking memory bank 5...
Checking memory bank 6...
Checking memory bank 7...

Test: Check SRAM Wed Mar 1 14:20:54 2023
Checking memory bank 0...
Checking memory bank 1...
Checking memory bank 2...
Checking memory bank 3...
Checking memory bank 4...
Checking memory bank 5...
Checking memory bank 6...
Checking memory bank 7...

Starting the test not parallel!!!

Test: Flash Diagnostics Wed Mar 1 14:21:05 2023
HDVI Revision Number:01600000
PAS Revision Number:01152609

Test: Flash Diagnostics Wed Mar 1 14:21:08 2023
HDVI Revision Number:01604005
PAS Revision Number:01152604

Starting the test not parallel!!!

Test: Group and Site Diagnostics Wed Mar 1 14:21:10 2023

Test: Group and Site Diagnostics Wed Mar 1 14:21:13 2023

Starting the test not parallel!!!

Test: Cal Usage Diagnostics Wed Mar 1 14:21:15 2023

Test: Cal Usage Diagnostics Wed Mar 1 14:21:25 2023

Starting the test not parallel!!!

Test: Check VIS capture memory Wed Mar 1 14:21:34 2023

Test: Check VIS capture memory Wed Mar 1 14:21:46 2023

Starting the test with parallel boards:

Test: Check VIS Waveform Wed Mar 1 14:21:59 2023

Starting the test not parallel!!!

Test: Check PAS Waveform Wed Mar 1 14:22:07 2023

Test: Check PAS Waveform Wed Mar 1 14:22:12 2023

Starting the test not parallel!!!

Test: Check events Wed Mar 1 14:22:18 2023

Test: Check events Wed Mar 1 14:22:23 2023

Starting the test not parallel!!!

Test: Check alarms Wed Mar 1 14:22:28 2023

Test: Check alarms Wed Mar 1 14:22:31 2023

Starting the test not parallel!!!

Test: Ganging relays Diagnostics Wed Mar 1 14:22:34 2023

Test: Ganging relays Diagnostics Wed Mar 1 14:22:36 2023

Starting the test not parallel!!!

Test: ADC Checker Wed Mar 1 14:22:39 2023

Test: ADC Checker Wed Mar 1 14:23:39 2023

Starting the test with parallel boards:

Test: HDVI Clean Up Test Wed Mar 1 14:24:39 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary HDVI Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 14:24:40 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 10 | PASS | 982-7642 | 00.LA | | PHX_HDVI |

11 | PASS | 982-7642 | 00.LA | | PHX_HDVI

|---
| Slot | Test Title
| 11 |
| 01 |
|---
| .. | CPI Load Port Test
| .. | CPI Data Sequencer Memory Test
| .. | Check local bus
| .. | Check high speed bus
| .. | Check VIS communication
| .. | Check PAS communication
| .. | Check SRAM
| .. | Flash Diagnostics
| .. | Group and Site Diagnostics
| .. | Cal Usage Diagnostics
| .. | Check VIS capture memory
| .. | Check VIS Waveform
| .. | Check PAS Waveform
| .. | Check events
| .. | Check alarms
| .. | Ganging relays Diagnostics
| .. | ADC Checker
| .. | HDVI Clean Up Test
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"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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***                                                  ***
***          Diagnostic (HDVI Calibration) Run Begins At          ***
***               Wed Mar 01 14:24:40 2023                ***
***                                                  ***
***                                                  ***
***                                                  ***
**********************************************************************************
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Test: DMM Rear Terminals Cal                    Wed Mar  1 14:24:40 2023

Starting the test not parallel!!!

Test: Check DMM                                 Wed Mar  1 14:24:44 2023
DMM found: HEWLETT-PACKARD,34401A
Checking Cables
==> Cables OK

Test: Check DMM                                 Wed Mar  1 14:24:51 2023
DMM found: HEWLETT-PACKARD,34401A
Checking Cables
==> Cables OK

Starting the test not parallel!!!

Test: General Calibration                       Wed Mar  1 14:24:57 2023
Init General calfactors
/tmp/HDVICalDataSet10.csv
Initial Calfile created.
Calfile loaded.
General calibration data saved.

Test: General Calibration                       Wed Mar  1 14:27:03 2023
Init General calfactors
/tmp/HDVICalDataSet11.csv
Initial Calfile created.
Calfile loaded.
General calibration data saved.

Starting the test with parallel boards:

Test: Channel Calibration                       Wed Mar  1 14:29:08 2023
/tmp/HDVICalDataSet10.csv
Calfile loaded.
/tmp/HDVICalDataSet11.csv
Calfile loaded.
CalData saved.
Save Calibration data in hardware
/tmp/HDVICalDataSet10.csv
Calfile loaded.
```

Erase Flash Cal memory
Save calibraton into Flash
Save Factors for PAS ch0
Save Factors for PAS ch1
Save Factors for ch0 to ch71
Finished
CalData saved.
Save Calibration data in hardware
/tmp/HDVICalDataSet11.csv
Calfile loaded.
Erase Flash Cal memory
Save calibraton into Flash
Save Factors for PAS ch0
Save Factors for PAS ch1
Save Factors for ch0 to ch71
Finished

Starting the test with parallel boards:

Test: HDVI Clean Up Test Wed Mar 1 15:31:01 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary HDVI Calibration PASS

Host Name FTDX005
Run Completed Wed Mar 01 15:31:01 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 10 | PASS | 982-7642 | 00.LA | | PHX_HDVI |
11 | PASS | 982-7642 | 00.LA | | PHX_HDVI

|---
| Slot | Test Title
|---|
| 11 |
| 01 |
|---
| .. | DMM Rear Terminals Cal
| .. | Check DMM
| .. | General Calibration
| .. | Channel Calibration
| .. | HDVI Clean Up Test
|---
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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*** Diagnostic (HDVI Verify) Run Begins At ***
*** Wed Mar 01 15:31:01 2023 ***
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Test: DMM Rear Terminals Vrf Wed Mar 1 15:31:01 2023

Starting the test with parallel boards:

Test: IFVM Verification Wed Mar 1 15:31:04 2023

Starting the test with parallel boards:

Test: VFIM Verification Wed Mar 1 15:34:29 2023

Starting the test with parallel boards:

Test: VClamps Verification Wed Mar 1 15:38:46 2023

Starting the test with parallel boards:

Test: IClamps Verification Wed Mar 1 15:38:58 2023

Starting the test not parallel!!!

Test: PAS Verification Wed Mar 1 15:39:21 2023

Test: PAS Verification Wed Mar 1 15:39:45 2023

Starting the test with parallel boards:

Test: HDVI Clean Up Test Wed Mar 1 15:40:09 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary HDVI Verify PASS

Host Name FTDX005

Run Completed Wed Mar 01 15:40:09 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
10	PASS	982-7642	00.LA		PHX_HDVI
11	PASS	982-7642	00.LA		PHX_HDVI

Slot	Test Title
11	
01	
..	DMM Rear Terminals Vrf
..	IFVM Verification
..	VFIM Verification
..	VClamps Verification
..	IClamps Verification
..	PAS Verification
..	HDVI Clean Up Test

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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********** Result Display Format Rev 01 **********
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Software Revision U5.2.2.1
Summary PASS

Host Name FTDX005
Run Completed Wed Mar 01 15:40:09 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
10	PASS	982-7642	00.LA		PHX_HDVI
11	PASS	982-7642	00.LA		PHX_HDVI

Slot	Test Title
11	
01	

|---------------------------
| .. | DMM Rear Terminals Vrf
| .. | IFVM Verification
| .. | VFIM Verification
| .. | VClamps Verification
| .. | IClamps Verification
| .. | PAS Verification
| .. | HDVI Clean Up Test
|---------------------------
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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########## SMC+ summary ##########

Loops : 1

Version: U5.2.2.1
Tester: FTDX005
Created: 2023-03-01 15:40:09

Session tasks	Instruments	Loadboards	Total	Aborted	Failed	Last Run	Duration (sec.)
All tasks			161	0	0	2023-03-01 15:40:09	15549.416
THCTL Checker	THCTL	Removed	15	0	0	2023-03-01 11:21:17	39.136
Board ID	THCTL	Removed	1	0	0	2023-03-01 11:20:36	00.004
THCTL Common Instrument Checker	THCTL	Removed	1	0	0	2023-03-01 11:20:37	01.472
Data Sequencer Memory Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:37	01.472
THCTL Instrument Checker	THCTL	Removed	12	0	0	2023-03-01 11:21:17	37.652
Register Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:37	00.004
Temperature Sensor Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:37	00.003
Fan Speed Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:38	00.002
Event Bus Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:39	01.409
Inter-IMA Event Bus Chk	THCTL	Removed	1	0	0		00.002
Cal Bus Relay Chk	THCTL	Removed	1	0	0	2023-03-01 11:20:41	01.235
Inter-IMA Cal and Analog Bus Chk	THCTL	Removed	1	0	0		

00.002 |
| TMU Mux Chk | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:20:41 |
00.003 |
| DC Force Voltage Chk | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:20:48 |
07.258 |
| DC Force Current Chk | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:16 |
27.287 |
| DC Measure Voltage Chk | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:16 |
00.437 |
| PLX EEPROM Config Chk | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:17 |
00.004 |
| Cleanup | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:17 |
00.005 |
| THCTL Calibration | THCTL | Removed | 5 | 0 | 0 | 2023-03-01 11:21:35 |
17.248 |
| DMM Rear Terminals Cal | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:19 |
02.087 |
| DC Force Voltage Cal | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:21 |
02.054 |
| DC Force Current Cal | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:34 |
12.508 |
| DC Measure Voltage Cal | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:35 |
00.591 |
| Cleanup | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:35 |
00.006 |
| THCTL Verify | THCTL | Removed | 5 | 0 | 0 | 2023-03-01 11:22:12 |
36.991 |
| DMM Rear Terminals Vrf | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:35 |
00.054 |
| DC Force Voltage Vrf | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:21:43 |
08.106 |
| DC Force Current Vrf | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:22:11 |
27.291 |
| DC Measure Voltage Vrf | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:22:12 |
01.533 |
| Cleanup | THCTL | Removed | 1 | 0 | 0 | 2023-03-01 11:22:12 |
00.006 |
| SDU Checker | SDU | Removed | 17 | 0 | 0 | 2023-03-01 11:22:18 |
03.533 |
| Board ID | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:13 | 00.003
|
| SDU Common Instrument Checker | SDU | Removed | 5 | 0 | 0 | 2023-03-01 11:22:15 |
01.519 |
| Cpi Load Port Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:13 |
00.020 |
| Temperature Sensor Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:13 |
00.003 |
| Data Sequencer Memory Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:14 |
01.467 |
| Internal Event Bus Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:15 |
00.003 |
| DMA Burst Write/Read Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:15 |
00.024 |
| SDU Instrument Checker | SDU | Removed | 11 | 0 | 0 | 2023-03-01 11:22:18 |

02.010 |
| register R/W Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:15 |
00.003 |
| reference clock status Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:15 |
00.005 |
| DUT Board CBIT Functional Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:15 |
00.024 |
| DUT Board Power Supply Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:17 |
01.402 |
| DC cal bus Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.561 |
| DUT board event bus Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| DUT board register bus Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| DUT board status signal Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| DUT board I2C bus Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| Scope sync Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| DUT board SPI Chk | SDU | Removed | 1 | 0 | 0 | 2023-03-01 11:22:18 |
00.002 |
| DPIN96 Checker | DD1096_32 | Removed | 43 | 0 | 0 | 2023-03-01 12:01:02 |
2316.629 |
| DPIN96 Common Instrument Checker | DD1096_32 | Removed | 3 | 0 | 0 | 2023-03-01
11:22:40 | 21.399 |
| Cpi Load Port Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:22:19 |
00.155 |
| Data Sequencer Memory Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:22:38 |
19.021 |
| DMM Rear Terminals Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:22:40 |
02.222 |
| DPIN96 Basic Checker | DD1096_32 | Removed | 4 | 0 | 0 | 2023-03-01 11:24:41 |
120.014 |
| Caldiag Init Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:22:43 |
02.126 |
| BI Health Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:22:43 |
00.112 |
| DMM Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:24:40 |
117.035 |
| Trigger Reflection Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:24:41 |
00.740 |
| DPIN96 Register/Memory Checker | DD1096_32 | Removed | 4 | 0 | 0 | 2023-03-01
11:25:29 | 47.264 |
| Base Register Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:24:56 |
15.336 |
| PATE Register Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:25:22 |
25.255 |
| Memory Controller Register/Memory Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01
11:25:22 | 00.092 |
| IC VPRAM exhaustive Memory Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01
11:25:29 | 06.580 |
| DPIN96 DC Checker | DD1096_32 | Removed | 6 | 0 | 0 | 2023-03-01 11:27:30 |

119.850 |
| ADC Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:25:29 |
00.286 |
| Driver Reference Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:25:54 |
24.364 |
| Driver Disable Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:26:23 |
28.919 |
| PMU FV/MV Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:26:46 |
22.915 |
| PMU FI/MI Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:27:10 |
24.046 |
| PMU Clamp Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:27:30 |
19.318 |
| DPIN96 MC Checker | DD1096_32 | Removed | 15 | 0 | 0 | 2023-03-01 11:57:04 |
1772.148 |
| MC DRAM R/W Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:29:33 |
123.351 |
| MC DRAM Walk-1 Addr Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01
11:29:34 | 00.281 |
| MC Capture Memory Stress Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:31:17
| 102.929 |
| MC Capture RAM Walk-1 Addr Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01
11:31:17 | 00.286 |
| MC Basic Pattern Burst Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:32:46 |
88.901 |
| MC MNO bit walk Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:33:03 |
16.416 |
| MC Repeat Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:34:07 |
63.688 |
| MC Progressive Repeat Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:34:26 |
19.032 |
| MC Loop Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:35:30 |
63.888 |
| MC Loop Boundary Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:36:34 |
64.079 |
| MC Fail on Last Vector Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:37:38 |
64.126 |
| MC Fail per Channel Diag Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:37:54 |
15.866 |
| MC Vector DRAM Integrity Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:39:38
| 103.160 |
| MC Capture RAM Integrity Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:42:52
| 194.661 |
| MC / Omni Multi-Period Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:57:04 |
851.478 |
| DPIN96 Scan Checker | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:57:39 |
34.685 |
| Basic Scan Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:57:39 |
34.685 |
| DPIN96 AC Checker | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:58:04 |
24.723 |
| AC Basic Chk | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 11:58:04 |
24.723 |
| DPIN96 DC Cal Pre-test Checker | DD1096_32 | Removed | 8 | 0 | 0 | 2023-03-01 12:01:01

| 176.470 |
DC Comparator Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 11:58:46	42.039
DC Driver Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 11:59:01	15.304
DC FV16 Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 11:59:17	15.174
DC Clamp Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 11:59:59	41.677
DC Measure Voltage Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 12:00:14	15.153
DC Force Current Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 12:00:31	16.558
DC Measure Current Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 12:00:46	15.307
DC Comparator Measure Current Pre-test Chk	DD1096_32	Removed	1	0	0	2023-03-01 12:01:01	15.256
Cleanup	DD1096_32	Removed	1	0	0	2023-03-01 12:01:02	00.071
DPIN96 Calibration	DD1096_32	Removed	14	0	0	2023-03-01 13:26:58	5154.049
DPIN96 Initialize Calibration files	DD1096_32	Removed	1	0	0	2023-03-01 12:01:02	00.141
DPIN96 DC Calibration	DD1096_32	Removed	11	0	0	2023-03-01 13:24:20	4996.431
DPIN96 DC Extern Calibration	DD1096_32	Removed	2	0	0	2023-03-01 12:17:12	969.246
DMM Rear Terminals Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:01:02	00.201
External DC Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:17:12	969.044
DPIN96 DC Calibration	DD1096_32	Removed	9	0	0	2023-03-01 13:24:20	4027.185
DC Gnd Level Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:17:28	16.478
DC Comparator Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:22:56	327.475
DC Driver Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:26:27	211.088
DC FV16 Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:29:16	168.912
DC Clamp Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:31:58	161.268
DC Measure Voltage Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:34:05	127.583
DC Force Current Cal	DD1096_32	Removed	1	0	0	2023-03-01 12:41:34	448.057
DC Measure Current Cal	DD1096_32	Removed	1	0	0	2023-03-01 13:10:35	1741.625
DC Comparator Measure Current Cal	DD1096_32	Removed	1	0	0	2023-03-01 13:24:20	824.694
AC Temperature Compensation Cal	DD1096_32	Removed	1	0	0	2023-03-01 13:26:58	157.404
Cleanup	DD1096_32	Removed	1	0	0	2023-03-01 13:26:58

00.071 |
| DPIN96 Verify | DD1096_32 | Removed | 11 | 0 | 0 | 2023-03-01 14:12:23 |
2723.013 |
| DPIN96 DC Verify | DD1096_32 | Removed | 10 | 0 | 0 | 2023-03-01 14:12:22 |
2722.900 |
| DMM Rear Terminals Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:26:58 |
00.199 |
| External DC Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:31:41 |
282.303 |
| DC Comparator Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:34:24 |
163.498 |
| DC Driver Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:35:22 |
57.350 |
| DC FV16 Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:35:52 |
29.635 |
| DC Clamp Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:37:49 |
117.537 |
| DC Measure Voltage Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:38:08 |
18.326 |
| DC Force Current Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 13:42:17 |
248.743 |
| DC Measure Current Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 14:00:19 |
1082.302 |
| DC Comparator Measure Current Vrf | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01
14:12:22 | 723.001 |
| Cleanup | DD1096_32 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:23 |
00.112 |
| DPS16 Checker | DPS16 | Removed | 5 | 0 | 0 | 2023-03-01 14:12:26 |
02.062 |
| DPS16 Common Instrument Checker | DPS16 | Removed | 2 | 0 | 0 | 2023-03-01 14:12:24
| 01.472 |
| Cpi Load Port Chk | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:23 |
00.008 |
| Data Sequencer Memory Chk | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:24 |
01.464 |
| DPS16 Instrument Checker | DPS16 | Removed | 2 | 0 | 0 | 2023-03-01 14:12:25 |
00.467 |
| Read FPGA Version Chk | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:25 |
00.144 |
| Memory Chk | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:25 |
00.322 |
| Cleanup | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:26 | 00.121
|
| DPS16 Calibration | DPS16 | Removed | 7 | 0 | 0 | 2023-03-01 14:13:41 |
74.718 |
| DMM Rear Terminals Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:28 |
02.076 |
| Ref Voltages and Resistors Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:49 |
20.751 |
| ADC Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:49 |
00.495 |
| Force Voltage Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:12:52 |
02.597 |
| Current Measure Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:13:16 |

23.959 |
| Current Limit Cal | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:13:41 |
24.707 |
| Cleanup | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:13:41 | 00.130
|
| DPS16 Verify | DPS16 | Removed | 9 | 0 | 0 | 2023-03-01 14:19:29 |
346.418 |
| ADC Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:13:42 |
00.168 |
| Force Voltage Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:13:44 |
02.356 |
| Measure Current Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:15:38 |
114.271 |
| Current Limit Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:18:32 |
173.391 |
| 4A Gang Mode Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:19:03 |
31.017 |
| 8A Gang Mode Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:19:19 |
15.577 |
| 16A Gang Mode Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:19:27 |
07.866 |
| Site control Vrf | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:19:29 |
01.648 |
| Cleanup | DPS16 | Removed | 1 | 0 | 0 | 2023-03-01 14:19:29 | 00.121
|
| HDVI Checker | HDVI | Removed | 18 | 0 | 0 | 2023-03-01 14:24:40 |
307.909 |
| HDVI Common Instrument Checker | HDVI | Removed | 2 | 0 | 0 | 2023-03-01 14:19:32 |
02.952 |
| Cpi Load Port Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:19:29 |
00.022 |
| Data Sequencer Memory Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:19:32 |
02.930 |
| HDVI Local Bus Checker | HDVI | Removed | 4 | 0 | 0 | 2023-03-01 14:20:40 |
67.023 |
| Local Bus Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:19:39 |
06.507 |
| High Speed Bus Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:19:43 |
04.499 |
| VIS Communication Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:20:35 |
51.543 |
| PAS Communication Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:20:40 |
04.473 |
| HDVI Memory Checker | HDVI | Removed | 2 | 0 | 0 | 2023-03-01 14:21:08 |
27.665 |
| SRAM Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:21:03 |
23.023 |
| Flash Diagnostics Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:21:08 |
04.642 |
| HDVI Memory Checker | HDVI | Removed | 7 | 0 | 0 | 2023-03-01 14:22:32 |
82.885 |
| Group and Site Diagnostics Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:21:13 |
05.038 |
| Usage Diagnostics Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:21:32 |

18.506 |
| VIS capture memory Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:21:56 |
24.574 |
| VIS Waveform Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:04 |
07.977 |
| PAS Waveform Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:16 |
10.987 |
| Events Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:26 |
10.079 |
| Alarms Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:32 |
05.720 |
| HDVI Relay Checker | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:36 |
04.672 |
| Ganging relays Diagnostics Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:22:36 |
04.672 |
| HDVI ADC Checker | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:24:37 |
120.404 |
| ADC Chk | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:24:37 |
120.404 |
| Cleanup | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:24:40 | 02.305
|
| HDVI Calibration | HDVI | Removed | 5 | 0 | 0 | 2023-03-01 15:31:01 |
3980.650 |
| DMM Rear Terminals Cal | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:24:42 |
02.088 |
| DMM Check | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:24:54 |
12.416 |
| General Cal | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 14:29:06 |
251.612 |
| Channel Cal | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:30:58 |
3712.217 |
| Cleanup | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:31:01 | 02.316
|
| HDVI Verify | HDVI | Removed | 7 | 0 | 0 | 2023-03-01 15:40:09 |
547.055 |
| HDVI Customer Verify | HDVI | Removed | 6 | 0 | 0 | 2023-03-01 15:40:07 |
544.753 |
| DMM Rear Terminals Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:31:01 |
00.067 |
| IFVM QuickCheck Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:34:27 |
205.408 |
| VFIM QuickCheck Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:38:44 |
257.153 |
| VClamps QuickCheck Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:38:56 |
11.411 |
| IClamps QuickCheck Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:39:18 |
22.641 |
| PAS QuickCheck Vrf | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:40:07 |
48.071 |
| Cleanup | HDVI | Removed | 1 | 0 | 0 | 2023-03-01 15:40:09 | 02.301
|

##

Slot[240]:Loadboard : LTXC_PHX_MAINSTREET,PN# 979-1337, SN# , VRev 00.GA

########## Build : U5.2.2.1 ##########
System Meter : slot 208 LTXC_HP34401A RACK RS232

########## LoadBoard : MAINSTREET ##########

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****
*** ***
*** ***
*** ***
*** Diagnostic (SDU Checker) Run Begins At ***
*** Wed Mar 01 16:30:41 2023 ***
*** ***
*** ***
*** ***
****
****
****
Test: Board ID Wed Mar 1 16:30:41 2023

IMA0_Slot0 Slot[0]:LTXC_PHX_SDU, PN# 979-1300, SN# , VRev 10.AF

Test: SDU DUT Board IO Test Wed Mar 1 16:30:43 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary SDU Checker PASS

Host Name FTDX005
Run Completed Wed Mar 01 16:30:58 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 0 | PASS | 979-1300 | 10.AF | | PHX_SDU

|---
| Slot | Test Title
|---|
| 0 |
|---

```
| .   | Board ID
| .   | SDU DUT Board IO Test
|--------------------------
"." = Pass   "F" = Fail   "u" = Untested   "A" = Aborted   " " = Not Applicable

********************************************************************************
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########## LoadBoard : MAINSTREET ##########


********************************************************************************
********************************************************************************
********************************************************************************
***                                          ***
***                                          ***
***                                          ***
***          Diagnostic (DPIN96 Checker) Run Begins At          ***
***               Wed Mar 01 16:30:58 2023                ***
***                                          ***
***                                          ***
***                                          ***
********************************************************************************
********************************************************************************
********************************************************************************
Test: DUT IO Test                          Wed Mar  1 16:30:58 2023
Test: DPIN96 Clean Up Test                 Wed Mar  1 16:32:10 2023


********************** Result Display Format Rev 01 ****************************
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Software Revision   U5.2.2.1
Summary             DPIN96 Checker PASS

Host Name           FTDX005
Run Completed       Wed Mar 01 16:32:13 2023

Support Boards      None

-----------------------------------------------------------------
| Slot |  Status  |    P/N      | VRev |    S/N     | Type
-----------------------------------------------------------------
|   2  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   3  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   4  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   5  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   6  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   7  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   8  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|   9  |   PASS   | 982-7419    | 00.OA |            | PHX_DD1096_32
|  12  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|  13  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
|  14  |   PASS   | 982-7419    | 00.PF |            | PHX_DD1096_32
```

| 15 | PASS | 982-7419 | 00.OA | | PHX_DD1096_32 |

16 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32

|---
| Slot | Test Title
|---|
| 11111 |
| 2345678923456 |
|---|---
| ..... | DPIN96 Clean Up Test
|---
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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########## LoadBoard : MAINSTREET ##########

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***
*** Diagnostic (Phx Digital Slot-to-Slot Timing CAL) Run Begins At ***
*** Wed Mar 01 16:32:13 2023 ***
***
***
***
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****
****
Test: Digital Slot2Slot Cal Wed Mar 1 16:32:13 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary Phx Digital Slot-to-Slot Timing CAL PASS

Host Name FTDX005
Run Completed Wed Mar 01 16:34:40 2023

Support Boards None

| Slot | Status | P/N | VRev | S/N | Type

| 2 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32 |
3 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32

| 4 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 5 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 6 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 7 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 8 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 9 | PASS | 982-7419 | 00.OA | | PHX_DD1096_32
| 12 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 13 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 14 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 15 | PASS | 982-7419 | 00.OA | | PHX_DD1096_32
| 16 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32

|---
| Slot | Test Title
|---|
| 11111 |
| 2345678923456 |
|---
| ..... | Digital Slot2Slot Cal
|---
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

****
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########## LoadBoard : MAINSTREET ##########

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****
*** ***
*** ***
*** ***
*** Diagnostic (Phx Digital Slot-to-Slot Timing Verify) Run Begins At ***
*** Wed Mar 01 16:34:40 2023 ***
*** ***
*** ***
*** ***
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****
****
Test: Digital Slot2Slot Vrf Wed Mar 1 16:34:40 2023

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary Phx Digital Slot-to-Slot Timing Verify PASS

Host Name FTDX005
Run Completed Wed Mar 01 16:35:36 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
2	PASS	982-7419	00.PF		PHX_DD1096_32
3	PASS	982-7419	00.PF		PHX_DD1096_32
4	PASS	982-7419	00.PF		PHX_DD1096_32
5	PASS	982-7419	00.PF		PHX_DD1096_32
6	PASS	982-7419	00.PF		PHX_DD1096_32
7	PASS	982-7419	00.PF		PHX_DD1096_32
8	PASS	982-7419	00.PF		PHX_DD1096_32
9	PASS	982-7419	00.OA		PHX_DD1096_32
12	PASS	982-7419	00.PF		PHX_DD1096_32
13	PASS	982-7419	00.PF		PHX_DD1096_32
14	PASS	982-7419	00.PF		PHX_DD1096_32
15	PASS	982-7419	00.OA		PHX_DD1096_32
16	PASS	982-7419	00.PF		PHX_DD1096_32

Slot	Test Title
11111	
2345678923456	
.....	Digital Slot2Slot Vrf

"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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****

********** Result Display Format Rev 01 **********
****

Software Revision U5.2.2.1
Summary PASS

Host Name FTDX005
Run Completed Wed Mar 01 16:35:36 2023

Support Boards None

Slot	Status	P/N	VRev	S/N	Type
2	PASS	982-7419	00.PF		PHX_DD1096_32
3	PASS	982-7419	00.PF		PHX_DD1096_32
4	PASS	982-7419	00.PF		PHX_DD1096_32
5	PASS	982-7419	00.PF		PHX_DD1096_32
6	PASS	982-7419	00.PF		PHX_DD1096_32

| 7 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 8 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 9 | PASS | 982-7419 | 00.OA | | PHX_DD1096_32
| 12 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 13 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 14 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32
| 15 | PASS | 982-7419 | 00.OA | | PHX_DD1096_32
| 16 | PASS | 982-7419 | 00.PF | | PHX_DD1096_32

|---
| Slot | Test Title
|---|
| 11111 |
| 2345678923456 |
|---
| ..... | Digital Slot2Slot Vrf
|---
"." = Pass "F" = Fail "u" = Untested "A" = Aborted " " = Not Applicable

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########## SMC+ summary ##########

Loops : 1

Version: U5.2.2.1
Tester: FTDX005
Created: 2023-03-01 16:35:36

Session tasks	Instruments	Loadboards	Total	Aborted	Failed	Last Run	Duration (sec.)
All tasks			6	0	0	2023-03-01 16:35:36	282.142
SDU Checker	SDU	MAINSTREET	2	0	0	2023-03-01 16:30:55	11.526
Board ID	SDU	MAINSTREET	1	0	0	2023-03-01 16:30:41	00.006
SDU Instrument Checker	SDU	MAINSTREET	1	0	0	2023-03-01 16:30:55	11.520
DUT IO Chk	SDU	MAINSTREET	1	0	0	2023-03-01 16:30:55	11.520
DPIN96 Checker	DD1096_32	MAINSTREET	2	0	0	2023-03-01 16:32:10	70.304
DPIN96 DUT IO Checker	DD1096_32	MAINSTREET	1	0	0	2023-03-01 16:32:08	70.235
DUT IO Chk	DD1096_32	MAINSTREET	1	0	0	2023-03-01 16:32:08	70.235
Cleanup	DD1096_32	MAINSTREET	1	0	0	2023-03-01 16:32:10	00.069
Phx Digital Slot-to-Slot Timing CAL	DD1096_32	MAINSTREET	1	0	0	2023-03-01 16:34:38	144.592
Phx Digital Slot Cal	DD1096_32	MAINSTREET	1	0	0	2023-03-01 16:34:38	144.592

| Phx Digital Slot-to-Slot Timing Verify | DD1096_32 | MAINSTREET | 1 | 0 | 0 | 2023-03-01 16:35:36 | 55.718 |
| Phx Digital Slot Vrf | DD1096_32 | MAINSTREET | 1 | 0 | 0 | 2023-03-01 16:35:36 | 55.718 |

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