

Loading Pe VCAL Data Calibrated on Mon Mar 10 18:30:06 2025 with release f7.29.4.1

Loading Dp VCAL Data Calibrated on Mon Mar 10 18:29:41 2025 with release f7.29.4.1

The test program is loaded [C:\nextest\f7.29.4.1|a130e37|using map type NC].

TestStarted(1)...

Collecting hardware information. This can take several seconds:

Collecting SPC data .

Collecting UCD data .

Collecting APB data .

Collecting RPT data .

Collecting UPT data .

Current Time is: Tue Mar 11 14:46:07 2025

- Site controller details -

Intel(R) Xeon(R) CPU E3-1275 v5 @ 3.60GHz, 15912MB total physical memory.

Windows Embedded Standard

7601.win7sp1_gdr.150202-1526

- Revision Codes for Boards -

*** Summary of board set for SA pn: 522408 rev: 7, Opt pn: 525419 ***

Site 1 (Chassis 1, Slot 1, Slice 1)

Board Name	PWB Number	PWB Rev	PWA Number	PWA Rev	Misc GBits	HSBEX GBits	RCM MBits	X bits	Y bits	D bits
HSB4DFE	1	517352	14		64 (2)	0 (0)	64 (1)	24*	24*	36*
PE256D_1	4	519703	8							
DP4FSER_1	1	522282	3							
BIB	1	515732	2							
APB	1	519710	6	Firmware Revision (DLP): 6.16.9 Firmware Revision (DLM): 1.8.1						
SPC	xxxxxx	xxxxxx	x	xxxxxx	x	Firmware Revision (DLP): 7.4.13 Firmware Revision (DLM): 1.8.0				

* NOTE: The ECR X, Y, D bits represent the maximum allowable configuration.

- UCD9090 Device Revs -

Device	Model	Rev	Date
HSB_UCD9090_56D	HSB4_56D	3	140827
HSB_UCD9090_59D	HSB4_59D	3	140827
PE1_UCD9090_56D	PE256_56D	8	140626
PE1_UCD9090_95D	PE256_95D	1	130403
PE1_UCD9090_107D	PE256_107D	1	130403
DP1_UCD9090_59D	DP4_59D	2	160323

- Pin Electronics IDs -

Chip Type	Chip Num	Chip ID
PE (PE8-1)	U1	1
PE (PE8-1)	U5	1
PE (PE8-1)	U9	1
PE (PE8-1)	U13	1
PE (PE8-1)	U2	1
PE (PE8-1)	U6	1
PE (PE8-1)	U10	1
PE (PE8-1)	U14	1

PE (PE8-1) U3 1

PE (PE8-1) U7 1

PE (PE8-1) U11 1

PE (PE8-1) U15 1

PE (PE8-1) U4 1

PE (PE8-1) U8 1

PE (PE8-1) U12 1

PE (PE8-1) U16 1

- Timing Generator IDs -

Chip Type	Chip Num	Chip ID
TG (Tango)	U33	4
TG (Tango)	U37	4
TG (Tango)	U41	4
TG (Tango)	U45	4
TG (Tango)	U34	4
TG (Tango)	U38	4
TG (Tango)	U42	4
TG (Tango)	U46	4
TG (Tango)	U35	4

TG (Tango) U39 4

TG (Tango) U43 4

TG (Tango) U47 4

TG (Tango) U36 4

TG (Tango) U40 4

TG (Tango) U44 4

TG (Tango) U48 4

- DP Board device IDs -

Chip Type	Chip Num	Chip ID
DPS (AD5560)	U75	S4
DPS (AD5560)	U76	S4
DPS (AD5560)	U77	S4
DPS (AD5560)	U78	S4
DPS (AD5560)	U79	S4
DPS (AD5560)	U80	S4
DPS (AD5560)	U81	S4
DPS (AD5560)	U82	S4
DPS (AD5560)	U83	S4
DPS (AD5560)	U84	S4

DPS (AD5560) U85 S4

DPS (AD5560) U86 S4

DPS (AD5560) U87 S4

DPS (AD5560) U88 S4

DPS (AD5560) U89 S4

DPS (AD5560) U90 S4

DPS (AD5560) U107 S4

DPS (AD5560) U108 S4

DPS (AD5560) U109 S4

DPS (AD5560) U110 S4

DPS (AD5560) U111 S4

DPS (AD5560) U112 S4

DPS (AD5560) U113 S4

DPS (AD5560) U114 S4

DPS (AD5560) U115 S4

DPS (AD5560) U116 S4

DPS (AD5560) U117 S4

DPS (AD5560) U118 S4

DPS (AD5560) U119 S4

DPS (AD5560) U120 S4

DPS (AD5560) U121 S4

DPS (AD5560) U122 S4

- HSB Temperatures -

Device temp

HSB_AIR_IN = 29

HSB_AIR_OUT = 24

HSB_EX1_SNSR = 37

HSB_EX1_DIODE = 47

HSB_EX2_SNSR = 34

HSB_EX2_DIODE = 42

HSB_CPUI_SNSR = 33

HSB_CPUI_DIODE = 40

- PE Temperatures -

Device = temp Device = temp Device = temp Device = temp

SL1_PE1 = 55 SL1_PE2 = 56 SL1_PE3 = 55 SL1_PE4 = 54

SL1_PE5 = 55 SL1_PE6 = 57 SL1_PE7 = 56 SL1_PE8 = 55

SL1_PE9 = 56 SL1_PE10 = 58 SL1_PE11 = 58 SL1_PE12 = 55

SL1_PE13 = 56 SL1_PE14 = 56 SL1_PE15 = 56 SL1_PE16 = 56

Device = temp Device = temp Device = temp Device = temp

SL1_TANGO1 = 50 SL1_TANGO2 = 44 SL1_TANGO3 = 50 SL1_TANGO4 = 46

SL1_TANGO5 = 52 SL1_TANGO6 = 52 SL1_TANGO7 = 51 SL1_TANGO8 = 55

SL1_TANGO9 = 52 SL1_TANGO10 = 52 SL1_TANGO11 = 49 SL1_TANGO12 = 48

SL1_TANGO13 = 51 SL1_TANGO14 = 48 SL1_TANGO15 = 57 SL1_TANGO16 = 51

Device temp

LIQ_IN = 40

LIQ_OUT = 35

AIR_BOTCTR = 45

AIR_LO = 33

AIR_HI = 32

PG1_SNSR = 31

PG1_DIODE = 34

PG2_SNSR = 32

PG2_DIODE = 34

- DP Temperatures -

Device = temp Device = temp Device = temp Device = temp

SL1_DPSA1 = 30 SL1_DPSA1X = 32 SL1_DPSA2 = 31 SL1_DPSA2X = 31

SL1_DPSA3 = 31 SL1_DPSA3X = 31 SL1_DPSA4 = 31 SL1_DPSA4X = 32

SL1_DPSA5 = 30 SL1_DPSA5X = 31 SL1_DPSA6 = 30 SL1_DPSA6X = 30

SL1_DPSA7 = 31 SL1_DPSA7X = 31 SL1_DPSA8 = 31 SL1_DPSA8X = 31

SL1_DPSB1 = 31 SL1_DPSB1X = 31 SL1_DPSB2 = 31 SL1_DPSB2X = 31

SL1_DPSB3 = 31 SL1_DPSB3X = 32 SL1_DPSB4 = 31 SL1_DPSB4X = 31

SL1_DPSB5 = 30 SL1_DPSB5X = 31 SL1_DPSB6 = 31 SL1_DPSB6X = 30

SL1_DPSB7 = 30 SL1_DPSB7X = 31 SL1_DPSB8 = 31 SL1_DPSB8X = 31

Device temp

AIR_TOPCTR = 29

AIR_ADT7461 = 29

- HSB Voltages -

Device = Volts Device = Volts Device = Volts Device = Volts

V0P75 = 0.750 V0P85_CPUI = 0.850 V1P0 = 1.002 V1P5 = 1.504

V1P5_DIMM_G1 = 1.607 V1P5_DIMM_G2 = 1.610 V3P3 = 3.313 V2P5_EX1 = 2.501

V2P5_EX2 = 2.494 V2P5_CPUI = 2.499 V3P3_CTRL = 3.307 V5P_CTRL = 5.023

V5P_CTRL_SB = 5.000 V5P_AUX = 5.168 V12P_CTRL = 12.003 V3P0_EX1 = 2.997

V3P0_EX2 = 2.998 V3P0_CPUI = 3.000 V0P9_EX1 = 0.902 V0P9_EX2 = 0.903

SPC_AGND = 0.000 V4P096_REF = 4.098 V12P_HSB = 11.998 V12P_HSK = 11.903

V12P_AUX = 11.969 V12P_SPC_OR = 11.657 SPC_V5P = 5.004 SPC_V5PF = 6.022

SPC_V4P_RAW = 3.801 SPC_V4P = 3.782 SPC_V16P5 = 16.507 SPC_V8M5 = -8.533

IPS_ISHARE = 1.134 V3P3_UCD = 3.304 V3P3_CTRL_SB = 3.284 V5P_AUX1 = 0.615

V5P_AUX2 = 0.832 V5P_AUX3 = 0.651 V5P_AUX4 = 0.855 V5P_AUX5 = 0.634

V5P_AUX6 = 0.890 V5P_AUX7 = 0.652 V5P_AUX8 = 0.879

- PE Voltages -

Device = Volts Device = Volts Device = Volts Device = Volts

V7P6_SL1 = 7.601 V7P6_SL2 = 7.600 V4M35_SL1 = -4.351 V4M35_SL2 = -4.350

V1P15_SL1-1 = 1.313 V1P15_SL1-2 = 1.310 V1P15_SL2-1 = 1.312 V1P15_SL2-2 = 1.314

V1P2A_SL1 = 1.201 V1P2A_SL2 = 1.202 V0P85_SL1 = 0.899 V0P85_SL2 = 0.901

V0P75_TG = 0.750 V0P75 = 0.751 V1P5_TG = 1.501 V1P5_SL1 = 1.501

V1P5_SL2 = 1.500 V1P8 = 1.806 V2P5_PE = 2.503 V2P5 = 2.504

V3P3 = 3.303 V3P0 = 3.003 V11P5 = 11.513 V17P5 = 17.462

V1P0_SL1 = 0.998 V1P0_SL2 = 0.995 SPC_V3P3 = 3.269

- DP Voltages -

Device = Volts Device = Volts Device = Volts Device = Volts

V7M75 = -10.010 V4M5 = -4.506 V1P2 = 1.199 V1P5 = 1.499

V2P5 = 2.500 V3P3 = 3.296 V15P0 = 16.752 V32P0 = 32.006

- Revision Codes for Memory Modules -

Module Name	DIMM Function	Base Number	PGM Number	PGM Rev	Misc GBits	RCM MBits	HSBEX GBits
PE1-DIMM-1 (J14)	ECR (0x3)	516726	519245	1	32		
PE1-DIMM-2 (J15)	ECR (0x3)	516726	519245	1	32		
PE1-RCM (J12)		514846	519637	3		64	

- Vendor Information for Standard DIMM Memory Modules -

Module Name	Dimm Vendor	Dimm Part Number	Dimm Rev	Dimm Date	Dram Vendor	Dram Rev
PE1-DIMM-1 (J14)	N/A	WD3CE84G16NISF-NXT	1	1720	N/A	N/A
PE1-DIMM-2 (J15)	N/A	WD3CE84G16NISF-NXT	1	1720	N/A	N/A
PE1-RCM (J12)	777681	NEXTEST	519637	3	N/A	N/A

- Revision Codes for FPGAs -

FPGA Name	SW Rev	HW Rev
CPUI Fpga	0x1	0x15
HD4 PG Fpga	0x2	0xd
HD4 PG Fpga	0x2	0xd
HD4 PG Fpga	0x2	0xd
HD4 PG Fpga	0x2	0xd
HD4 DP Fpga	0x1	0x21
HD4 DP Fpga	0x1	0x21
HD4 DP Fpga	0x1	0x21
HD4 DP Fpga	0x1	0x21
HD4 ECR-EX FPGA	0x1	0x12
HD4 ECR-EX FPGA	0x1	0x12
HD4 ECR-EX FPGA	0x1	0x12
HD4 ECR-EX FPGA	0x1	0x12

Nextest software release: C:\nextest\f7.29.4.1\Bin\Ui.exe

Running on MagnumV_EV_System system# 900282

HOST computer name: fmnsgmag5011

PCC = 116

PRC = 0

Delta = 116

Calibration Time and Environment :-

Cal Cal Host Software Cal Hsb Ipc Dp Dp Pe Pe

Module Chas/Slot/Mod Date Time Name Relase

DP_1 1 1 1 Mon Mar 10 18:29:41 2025 FMNSGMAG5011 f7.29.4.1 7

PE_1 1 1 1 Mon Mar 10 18:30:06 2025 FMNSGMAG5011 f7.29.4.1 1

Information saved to c:\nextest\logs\BoardRevs

TestDone...bin = builtin_Pass,builtin_Pass,builtin_Pass,builtin_Pass

The test program is unloaded